1987 INDEX OF ARTICLES

This index serves as a guide to the important editorial material that appeared in ELECTRONIC DESIGN's 30 issues in 1987, including our six Special Issues.

For quick and easy reference, the editors have organized the articles in 15 subject categories—alphabetized below—along with their respective page numbers in the index. Within each category, the articles are alphabetized by title. In addition to the title each listing shows the issue date and page number.

We also give the source (company name) for each item. If the editorial material is a contributed technical article, then the company for which the author works is the source. For staff-written reports (and for any article that refers to more than two companies), we indicate "various" as the source because those reports deal with developments or products from many different manufacturers. In all other cases, the source of the article is the company responsible for the development being reported.

Each listing also notes whether the article was a staff-written special report, a contributed technical article, a new product announcement, or a news story. Throughout the index, we use a simple code (listed alphabetically below) to identify the type of article.

The article code reflects the department and section names in ELECTRONIC DESIGN; some have since changed. Briefly, these codes are:

- CP for Conference Preview, highlights of the technical program at a major upcoming industry meeting.
- DA for Design Application, a contributed technical article describing how recent products or techniques are applied.
- DE for Design Entry, a contributed article that describes in depth an important new design concept.
- DI for Design Innovation, a news story about an important new technology or about a significant advance in existing technology. (IN for Industry Newsmaker, an earlier version of our Design Innovation.)
- DP for Design Perspective, a staff-written perspective on an important new technology trend. (TP for Technology in Perspective and NF for Newsfront, earlier variations of what are now Design Perspectives.)
- NP for New Product, feature coverage of a significant new product announcement.
- PR for product report, a roundup of important products in a particular area of technology.
- SR for Special Report, a staff-written analysis of trends and developments in a specific technology area.

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NF	Newsfront
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CAE tools break barriers of feedback measurement (Analog Design Tools) . . . DA, *May 28, p. 117*

CAE users want open systems, integration (various) . . . DI, *Sept. 3, p. 37*

Chip design tool operates down to 1.25 μm (Valid Logic Systems) . . . NP, *Aug. 20, p. 124*

Clipper-based platforms speed up CAE graphics (Intergraph) . . . NP, *Aug. 6, p. 150*

Demands on simulators escalate as circuit complexity explodes (various) . . . SR, *Oct. Sp., p. 11*

Design management software cuts project-coordination headaches (EDA Systems) . . . DI, *June 11, p. 67*

Design real-time software that works the first time (Ready Systems) . . . DA, *Oct. 15, p. 105*

Design tools master embedded software's real-time demands (Ready Systems) . . . DI, *Sept. 17, p. 65*

Design tools model transformer magnetics (Analog Design Tools) . . . NP, *Oct. 29, p. 147*

Design tools race ahead . . . at a snail's pace (various) . . . DP, *April 30, p. 45*

Design-to-test links foster IC development (various) . . . DP, *Oct. 29, p. 33*

Designers build smarts into their CAE tolls with expert-system shells (various) . . . SR, *July 9, p. 71*

Desktop system checks prototype ASIC devices (Integrated Measurement Systems) . . . IN, *Feb. 5, p. 47*

Differences blur as PCs take on engineering workstations (various) . . . SR, *Sept. Sp., p. 15*

Do your homework before tying to surface-mounting CAE system (Philips) . . . DA, *Sept. Sp., p. 65*

Expert systems poised to reshape industry (various) . . . DP, *Mar. 19, p. 29*

Fault evaluator traces critical paths (Aida) . . . NP, *Nov. 12, p. 207*

Fault simulation runs on a PC card (Aida) . . . NP, *Mar. 5, p. 162*

Features simplify board, IC designing (Mentor Graphics) . . . NP, *June 11, p. 208*

Gate-array design system automates chip layout with integrated software (Tektronix) . . . NP, *April 30, p. 128*

Graphics chips, boards bring CAD to personal computers (various) . . . TP, *Feb. 19, p. 67*

Graphics systems boost performance (Hewlett-Packard) . . . NP, *Oct. 29, p. 147*

Hardware description language sculpts complex circuits (CAD Language Systems) . . . DA, *Aug. 6, p. 119*

Hierarchical-level CAE system features snap and simplicity (Hewlett-Packard) . . . DA, *Feb. 19, p. 113*

IC layout tools automate design (ECAD) . . . NP, *July 9, p. 133*

ICCAD: Logic synthesis and parallel processing (various) . . . DP, *Nov. 12, p. 33*

Interface weds mixed simulation (Daisy Systems) . . . NP, *Feb. 19, p. 134*

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Layout synthesis promises tightly packed ICs quickly (Caeco) . . . DA, *Nov. Sp., p. 55*

Library blends analog/digital cells (VTC) . . . NP, *May 14, p. 194*

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Low-cost logic simulator brings speed to the bench (Orcad Systems) . . . NP, *Aug. 20, p. 123*

Memory system helps PC lay out larger boards (Case Technology) . . . NP, *Mar. 19, p. 213*

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Module generation speeds IC design, extends ASIC library (various) . . . SR, *Nov. Sp., p. 17*

Object-oriented database keeps the house in order (Data General) . . . DE, *Mar. 19, p. 129*

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PC software routes surface-mounted devices (Personal CAD Systems) . . . DI, *Mar. 19, p. 54*

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Real-time software-design tool combines three different views (Ilogix) . . . DI, *July 23, p. 51*

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Silicon compiler serves up more design options (Silicon Compilers) . . . NF, *Mar. 5, p. 32*

Simulation verifies system designs faster (Logic Automation) . . . SR, *May Sp., p. 49*

Smart silicon compiler trims circuit fat (Silicon Compiler Systems) . . . NP, *April 16, p. 135*

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Statistical software finds timing errors and suggests fixes (Aldec) . . . DA, *Oct. Sp., p. 75*

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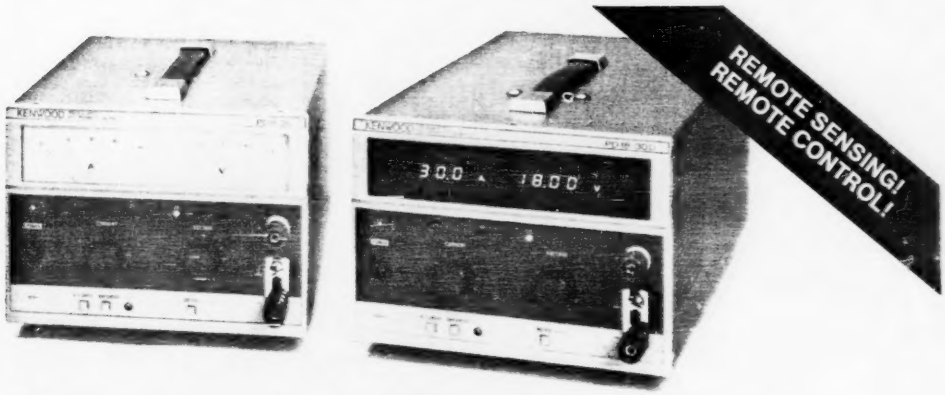

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